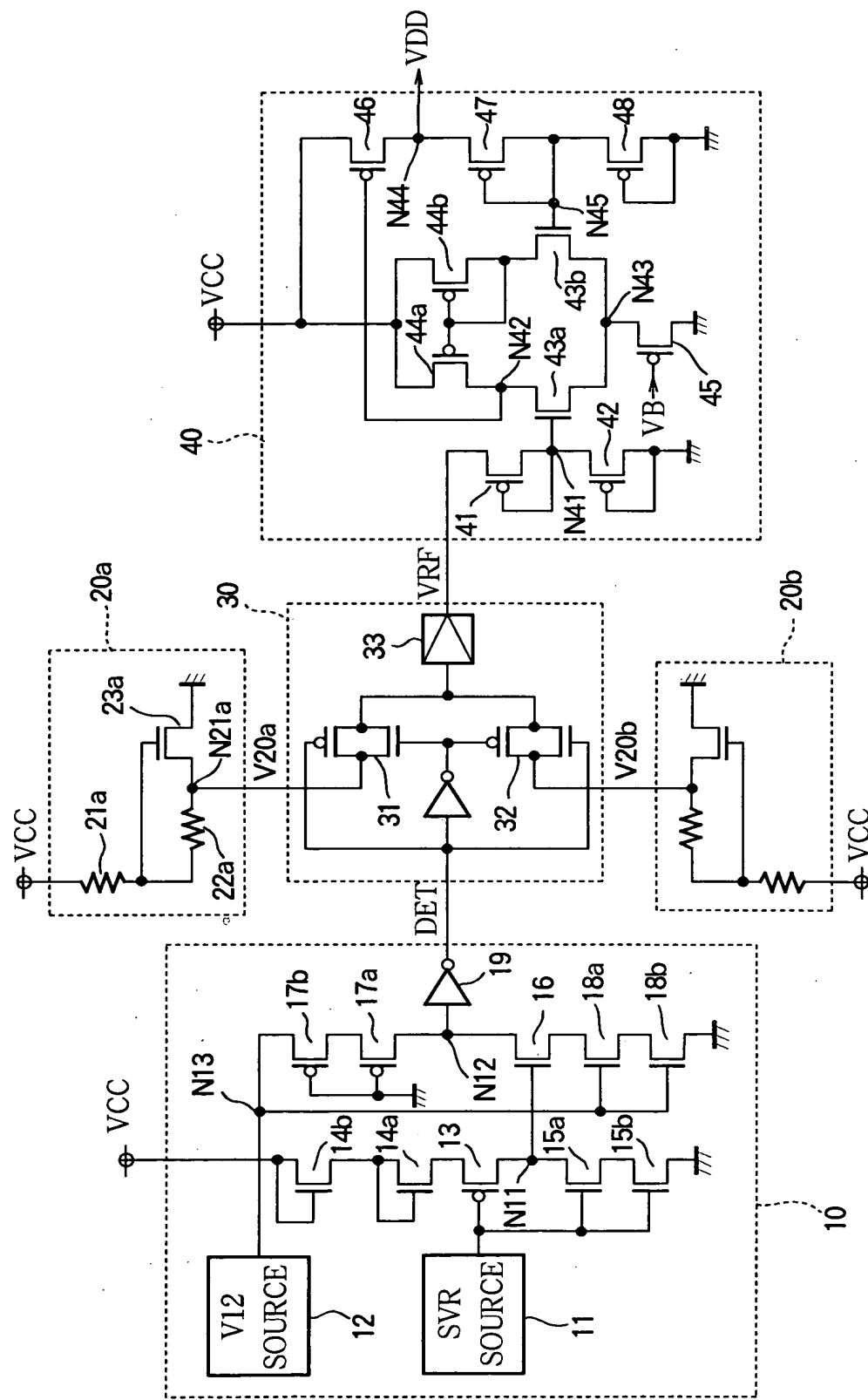


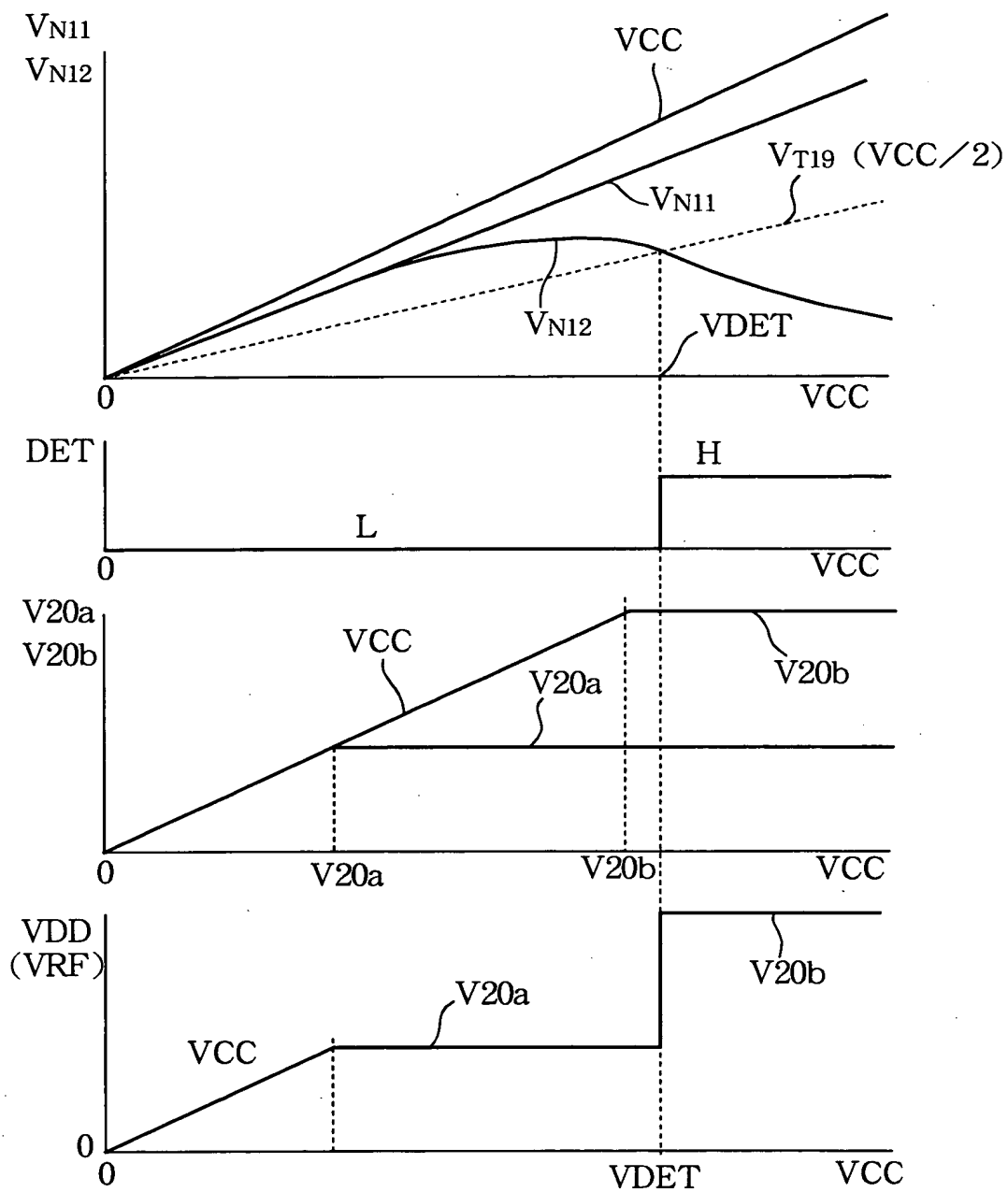
The diagram illustrates a semiconductor device 100, which is composed of several interconnected blocks and components:

- Block 10:** Contains two signal sources, **SVR SOURCE** (11) and **VI2 SOURCE** (12). It includes a series of transistors (N11, N12, N13) and resistors (13, 14a, 14b, 15a, 15b, 16, 17a, 17b, 18a, 18b) connected to a **VCC** supply.
- Block 20a:** A resistor (21a) and a transistor (N21a) connected to **VCC** and ground.
- Block 20b:** A resistor and a transistor connected to **VCC** and ground.
- Block 30:** A central processing block containing a differential pair of transistors (31, 32) with a current source (33) and a feedback loop (34). It is connected to **V20a** and **V20b**.
- Block 40:** A complex circuit block containing multiple transistors (N41, N42, N43, N44, N45) and resistors (41, 42, 43a, 43b, 44a, 44b, 45, 46, 47, 48). It is connected to **VCC**, **VDD**, and **VB**.

The diagram also shows various signal lines and connections, including **DET**, **VRF**, and **VB**.



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FIG.2



5/6
FIG.5

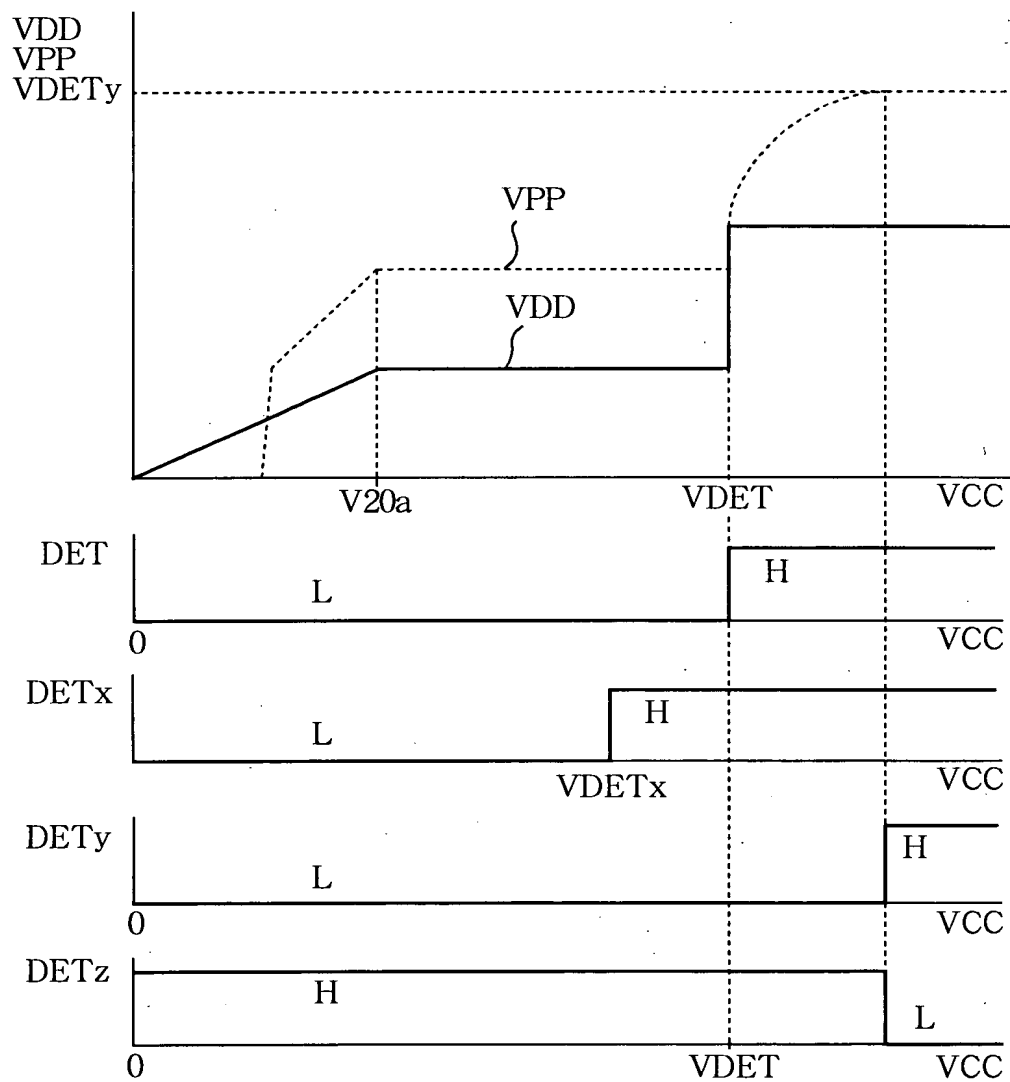


FIG. 6

